

PVMC: Programmable Vector Memory Controller

Authors: [Hussain, Tassadaq](#) / [Palomar, Oscar](#) / [Unsal, Osman](#) / [Cristal, Adrián](#) / [Aiguade, Eduard](#) / [Valero, Mateo](#)

Teams: [Computer Architecture For Parallel Paradigms](#) / [Resilience In Distributed Systems](#)

Publication: IEEE 25th International Conference on Application-Specific Systems, Architectures and Processors (ASAP 2014)

Place Published: Zurich, Switzerland

Pagination: 240?247

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 25 abr 2024 - 12:29): <https://www.bsc.es/ca/node/40880>