56_CS_OSCAI_RE1

Job Reference

56_CS_OSCAI_RE1

Position

Junior Research Engineer - Computer Sciences (RE1)

Data de tancament

Dilluns, 30 Juliol, 2018

About BSC

The Barcelona Supercomputing Center - Centro Nacional de Supercomputación (BSC-CNS) is the leading supercomputing center in Spain. It houses MareNostrum, one of the most powerful supercomputers in Europe, and is a hosting member of the PRACE European distributed supercomputing infrastructure. The mission of BSC is to research, develop and manage information technologies in order to facilitate scientific progress. BSC combines HPC service provision and R&D into both computer and computational science (life, earth and engineering sciences) under one roof, and currently has over 500 staff from 44 countries.

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BSC-CNS YouTube Channel
BSC-CNS Corporate Video
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Context And Mission

The Computer Architecture and Operating System group at the Barcelona Supercomputing Center aims at carrying out research on programming models for critical embedded systems in charge of controlling fundamental parts of cars, airplanes and satellites. Our work is mainly done in the context of bilateral projects with several processor companies as well as several European-funded projects. For a complete list of publications of the group in the last years, please visit: www.bsc.es/caos

The objective of this position is to research in the field of real-time systems. More specifically, the goal is to develop methodologies to test the reliability and security of safety-critical systems and to devise hardware mechanisms to increase the safety and security of the processor designs used in these applications.

Key Duties
1. Porting of existing open-source processors to FPGA.
2. Developing power and performance metering methodologies for these processor designs relying on existing FPGA monitors and EDA tools.
3. Test the security properties of these designs in front of power/timing side-channel attacks.

Requirements

- **Education**
  - Bachelor’s degree in Computer Sciences or related field
- **Essential Knowledge and Professional Experience**
  - Knowledge on C/C++ programming, FPGA prototyping
- **Competences**
  - Problem-solving, pro-active, result-oriented work attitude
  - Good communication skills including a good command of the English language (written and spoken)

Conditions

- The position will be located at BSC within the Computer Sciences Department
- We offer a full-time contract, a good working environment, a highly stimulating environment with state-of-the-art infrastructure, flexible hours, extensive training plan, tickets restaurant, private health insurance, fully support to the relocation procedures
- Salary: we offer a competitive salary commensurate with the qualifications and experience of the candidate and according to the cost of living in Barcelona
- Starting date: 01/07/2018

Applications Procedure

All applications must include:

- A motivation letter with a statement of interest, including two contacts for further references - COMPULSORY - Applications without this document will not be considered
- A full CV including contact details

Deadline

The vacancy will remain open until suitable candidate has been hired. Applications will be regularly reviewed and potential candidates will be contacted.

Diversity and Equal Opportunity Employment

BSC-CNS is an equal opportunity employer committed to diversity and inclusion. We are pleased to consider all qualified applicants for employment without regard to race, color, religion, sex, sexual orientation, gender identity, national origin, age, disability or any other basis protected by applicable state or local law.

Barcelona Supercomputing Center - Centro Nacional de Supercomputación