2_20_CS_PM_RE1

Job Reference

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Position

Developer Engineer - OmpSs@FPGA (RE1)

Data de tancament

Diumenge, 12 Gener, 2020
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About BSC

The Barcelona Supercomputing Center - Centro Nacional de Supercomputación (BSC-CNS) is the leading supercomputing center in Spain. It houses MareNostrum, one of the most powerful supercomputers in Europe, and is a hosting member of the PRACE European distributed supercomputing infrastructure. The mission of BSC is to research, develop and manage information technologies in order to facilitate scientific progress. BSC combines HPC service provision and R&D into both computer and computational science (life, earth and engineering sciences) under one roof, and currently has over 650 staff from 49 countries.

Look at the BSC experience:
BSC-CNS YouTube Channel
Let's stay connected with BSC Folks!

Context And Mission

The Parallel Programming Models (PM) group as part of the Computer Science Department, is one of the biggest groups composed of more than 30 PhD students, support engineers, postdocs and senior researchers. PM has a strong background on parallel and distributed programming models, domain specific languages (DSLs), compilers and accelerators. The PM group is involved in many EU project such as LEGaTO, EuroEXA, EPEEC, and DEEP-EST, as well as, industrial projects with Repsol, IBM, Intel, Microsoft, NVIDIA, and Fujitsu. The successful applicant will be based at BSC, but will also interact with our academic and industrial partners.

The PM group is looking for scientists from the national and international community who wish to develop his/her career in a stimulating environment, rich in technological resources and campus life. We encourage applications from highly motivated computer scientists with outstanding qualifications. Successful
candidates will join the PM research group to carry out their research in cutting-edge areas of parallel and distributed programming models, including the implementation of high-performance codes, heterogeneous systems, compilers, runtimes, accelerators, DSLs, etc.

**Key Duties**

- Developing support for new OmpSs@FPGA task offloading and dataflow/stream functionalities over new systems developed in the projects the group is involved.
- Support for application porting to OmpSs@FPGA for projects partners.
- Debugging and maintenance of codes.
- Participating on group/project publications, dissemination and training.

**Requirements**

- **Education**
  - Bachelor or master in Computer Sciences, Computer Engineering, Telecommunications Engineering, or equivalent.

- **Essential Knowledge and Professional Experience**
  - Good skills in programming in C, C++, OpenMP, and MPI.
  - Knowledge about compilers and task based runtime systems.
  - Knowledge about Verilog/VHDL, and high-level synthesis tools is highly recommendable.

- **Competences**
  - Good written and verbal communication skills (English/Spanish).
  - Ability to take initiative, prioritize and work under set deadlines and pressure.
  - Ability to work independently and in a team.

**Conditions**

- The position will be located at BSC within the Computer Sciences Department
- We offer a full-time contract, a good working environment, a highly stimulating environment with state-of-the-art infrastructure, flexible working hours, extensive training plan, tickets restaurant, private health insurance, fully support to the relocation procedures
- Duration: Temporary - 1 year renewable renewable
- Salary: we offer a competitive salary commensurate with the qualifications and experience of the candidate and according to the cost of living in Barcelona
- Starting date: 01/02/2020

**Applications Procedure**

All applications must include:

- A Cover Letter with a statement of interest in English, including two contacts for further references - Applications without this document will not be considered
- A full CV in English including contact details
Deadline

The vacancy will remain open until suitable candidate has been hired. Applications will be regularly reviewed and potential candidates will be contacted.

Diversity and Equal Opportunity Employment

BSC-CNS is an equal opportunity employer committed to diversity and inclusion. We are pleased to consider all qualified applicants for employment without regard to race, color, religion, sex, sexual orientation, gender identity, national origin, age, disability or any other basis protected by applicable state or local law.

This position is reserved for candidates who meet the requirements and have the legal status of disabled persons with a degree of disability equal to or greater than 33%. In case there are no applicants with disabilities that meet the requirements, the rest of the candidates without declared disability will be evaluated.

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

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