Applications are invited for a master student in High Performance Computing (HPC) architectures at the Barcelona Supercomputing Center (BSC). BSC intends to pave the way to the future low-power European processor for Exascale in the context of multiple architecture initiatives (EPI, DRAC, MEEP, eProcessor, Intel, etc.).

Key Duties

- Define a low-power System-on-Chip architecture targeting Exascale.
- Benchmark RISC-V-based HPC processors with relevant applications from the HPC domain as well.
as emerging applications such as precision medicine.

**Requirements**

- **Education**
  - B.Sc. in Computer Science. Applications from students already enrolled in a M.Sc. program in Computer Science are welcome.

- **Essential Knowledge and Professional Experience**
  - Computer Architecture: microarchitecture, resource sharing in multicores, cache hierarchy.
  - Operating Systems: Linux, scripting, OS scheduler.
  - Performance Analysis and Tuning of parallel applications.
  - Programming: C/C++, Assembler, Open MP/MPI, etc.

- **Additional Knowledge and Professional Experience**
  - Excellent written and verbal communication skills in English.

- **Competences**
  - Ability to take initiative, prioritize and work under set deadlines pressure.
  - Ability to work independently and in a team.
  - Capacity to interact and build strong relations with other research groups.

**Conditions**

- The position will be located at BSC within the Computer Sciences Department
- We offer a full-time contract, a good working environment, a highly stimulating environment with state-of-the-art infrastructure, flexible working hours, extensive training plan, tickets restaurant, private health insurance, fully support to the relocation procedures
- Duration: Open-ended contract due to technical and scientific activities linked to the project and budget duration
- Salary: we offer a competitive salary commensurate with the qualifications and experience of the candidate and according to the cost of living in Barcelona
- Starting date: 01/10/2022
Applications procedure and process

All applications must be made through BSC website and contain:

- A full CV in English including contact details
- A Cover Letter with a statement of interest in English, including two contacts for further references - Applications without this document will not be considered

In accordance with the OTM-R principles, a gender-balanced recruitment panel is formed for every vacancy at the beginning of the process. After reviewing the content of the applications, the panel will start the interviews, with at least one technical and one administrative interview. A profile questionnaire as well as a technical exercise may be required during the process.

The panel will make a final decision and all candidates who had contacts with them will receive a feedback with details on the acceptance or rejection of their profile.

At BSC we are seeking continuous improvement in our recruitment processes, for any suggestions or feedback/complaints about our Recruitment Processes, please contact recruitment [at] bsc [dot] es.

For more information follow this link

Deadline

The vacancy will remain open until suitable candidate has been hired. Applications will be regularly reviewed and potential candidates will be contacted.

OTM-R principles for selection processes

BSC-CNS is committed to the principles of the Code of Conduct for the Recruitment of Researchers of the European Commission and the Open, Transparent and Merit-based Recruitment principles (OTM-R). This is applied for any potential candidate in all our processes, for example by creating gender-balanced recruitment panels and recognizing career breaks etc.

BSC-CNS is an equal opportunity employer committed to diversity and inclusion. We are pleased to consider all qualified applicants for employment without regard to race, color, religion, sex, sexual orientation, gender identity, national origin, age, disability or any other basis protected by applicable state or local law.

For more information follow this link

Barcelona Supercomputing Center - Centro Nacional de Supercomputación