

PATC: Introduction to Heterogeneous Memory Usage

Objectives

The objective of this course is to learn how to use systems with more than one memory subsystem. We will see the different options on using Intel's KNL memory subsystems and systems equipped with Intel's Optane technology.

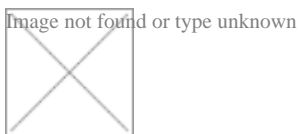
Requirements

Basic skills in C programming.

Learning Outcomes

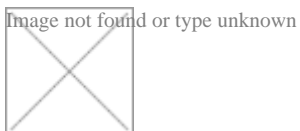
The students who finish this course will be able to leverage applications using multiple memory subsystems.

Academic Staff



Convener: Antonio Peña, Computer Sciences Senior Researcher, Accelerators and Communications for High Performance Computing, BSC

Further information



**All PATC Courses at BSC do not charge fees.
PLEASE BRING YOUR OWN LAPTOP.**

Your laptop does not need a particular GPU. An ssh client is required to connect to our GPU-equipped servers.

Recommended Accomodation: Please follow [the link](#) for map of some local hotels.

[CONTACT US](#) for further details about MSc, PhD, Post Doc studies, exchanges and collaboration in education and training with BSC.

For further details about Postgraduate Studies in UPC - Barcelona School of Informatics (FiB), visit the [website](#).

Sponsors: BSC and PRACE 5IP project are funding the PATC @ BSC training events. If you want to learn more about PRACE Project, visit the [website](#).

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

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