

[PATC Course: Programming ARM based Prototypes](#)

Objectives

Get a comprehensive view of the architecture of the heterogeneous ARM-based prototypes at BSC (ARM multicore cluster and ARM+ GPU cluster) and how to program these systems efficiently.

Requirements

Standard HPC users, with some knowledge in ARM CPU/GPU architecture (recommended, but not required).

[Registration for this course is available here](#)

**All PATC Courses at BSC do not charge fees.
PLEASE BRING YOUR OWN LAPTOP.**

Comments:

Please fill in the **evaluation form** by following [the link](#).

Recommended Accomodation:

Please follow [the link](#) for map of some local hotels.

Contact Us:

[CONTACT US](#) for further details about MSc, PhD, Post Doc studies, exchanges and collaboration in education and training with BSC.

For further details about Postgraduate Studies in UPC - Barcelona School of Informatics (FiB), visit the [website](#).

Sponsors:

BSC and PRACE 3IP project are funding the PATC @ BSC training events. If you want to learn more about PRACE Project, visit the [website](#).

[Materials](#)

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