

## PATC Course: Programming ARM based prototypes

### Objectives

Get a comprehensive view of the architecture of the ARM-based prototypes at BSC (ARM multicore cluster and ARM+CUDA GPU cluster) and how to program these machines efficiently.

#### **Learning Outcomes:**

Understand how ARM multicore and ARM+GPU clusters work. Work with some example codes, compiling, debugging and executing trough system queues.

Get a global view of the machines with their specific configuration (Operating system, batch system, debuggers, compilers, how to access to the machines, how to use the resources, etc)



### Requirements

The trainees are expected to come with their own laptop with a SSH client.



# PATC Course: Programming ARM based prototype

The registration for this course opened on 1st of October 2013.

## Recommended Accomodation:

Please follow [the link](#) for map of some local hotels.

## Contact Us:

[CONTACT US](#) for further details about MSc, PhD, Post Doc studies, exchanges and collaboration in education and training with BSC.

For further details about Postgraduate Studies in UPC - Barcelona School of Informatics (FiB), visit the [website](#).

## Sponsors:

The PATC@BSC training events are funded by BSC and PRACE 3IP project.

If you want to learn more about PRACE Project, visit the [website](#).

## [Materials](#)

Image not found or type unknown



This is a short video of a lecture.

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 1 des 2023 - 07:19):** <https://www.bsc.es/ca/education/training/patc-courses/patc-course-programming-arm-based-prototypes>