PATC: 30th VI-HPS Tuning Workshop

Objectives

Objectives: VI-HPS Tuning Workshops are the major training vehicle where around 40 participants receive instruction and guidance to become familiar with VI-HPS programming tools suite. During the training, we will explain the functionality of each tool and how they can be used. We will also give them hands-on assistance on how to apply VI-HPS tools to their own parallel application codes, along with advice for potential corrections and optimizations.

Additionally, feedback to tools developers helps direct tools development to user needs, as well as improve tool documentation and ease of use.

For further information please check the VI-HPS website

Requirements

Prerequisites: Basic knowledge on parallel programming. Attendees have to bring an application that can use on the hands-on. They need to have the source code and install it in the BSC machine.

Learning Outcomes

Learning Outcomes:

- Understand common performance analysis and diagnosis techniques and how they can be employed in practice
- Make sense of the functionality of individual tools, and how to use them effectively
- Apply VI-HPS tools to their own parallel application codes
- Optimize their own codes thanks to the coaching of how to tune their measurement and analysis
Course Convener: Judit Gimenez, Tools Group Manager, Computer Sciences - Performance Tools, BSC

Materials

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Further information

All PATC Courses at BSC do not charge fees.
NOTE: PLEASE BRING YOUR OWN LAPTOP.

Recommended Accommodation

Please follow the link for map of some local hotels.

CONTACT US for further details about MSc, PhD, Post Doc studies, exchanges and collaboration in education and training with BSC.
For further details about Postgraduate Studies in UPC - Barcelona School of Informatics (FiB), visit the website.

Sponsors: BSC and PRACE 4IP project are funding the PATC @ BSC training events.
If you want to learn more about PRACE Project, visit the website.

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