



**Barcelona
Supercomputing
Center**

Centro Nacional de Supercomputación

BSC Demos

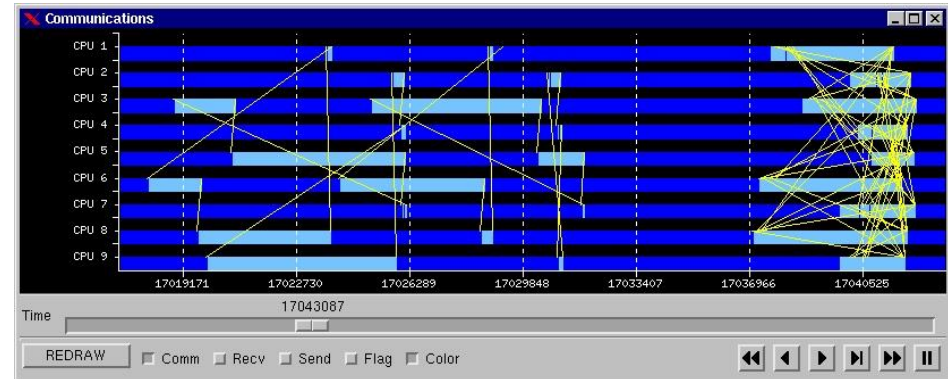


Interested in any of these topics?

Ask for a demo

« OmpSs Development Environment and Training Material

- OmpSs Examples:
- Debugging
- What to taskify?
- Paraver



« Parallel Python at Heterogenous Architectures

- PyCOMPSs Automatic parallelization
- Xeon and Xeon Phi
- Neuroscience application

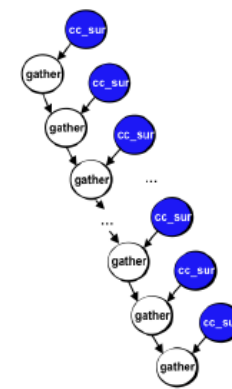
Tasks definition

```
@task(originals_file = FILE_INOUT, surrs_file = FILE_INOUT)
def gather(result, originals_file, surrs_file):
    # Accumulate results

@task(returns = list)
def cc_surrogate_range(start_idx, end_idx, seed, num_neurons, num_surrs, num_bins,
maxlag):
    # Compute neuron correlations
```

Main program

```
originals_file = 'result_cc_originals.dat'
surrs_file = 'result_cc_surrogates_conf.dat'
for frag in range(num_frags):
    start_idx = end_idx
    end_idx = int((frag+1)*step,num_ccs))
    result = cc_surrogate_range(start_idx, end_idx, seed, num_neurons,
num_surrs, num_bins, maxlag)
    gather(result, originals_file, surrs_file)
    seed = seed + delta
```

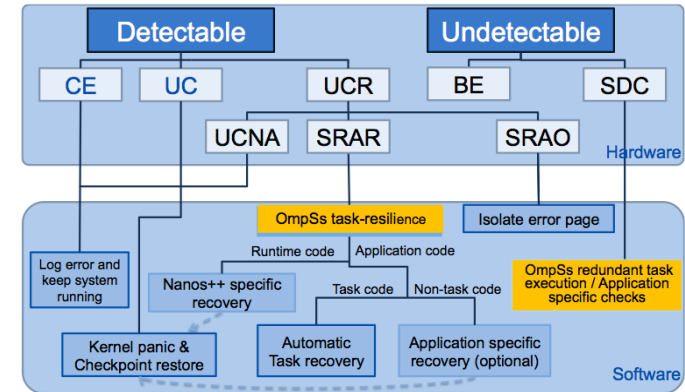


The diagram illustrates a task dependency graph. It consists of a vertical chain of nodes. The top node is labeled 'gather'. Below it are several nodes labeled 'cc_sur'. The nodes are connected by arrows pointing downwards, indicating a sequential flow of tasks. The graph shows how the 'gather' task depends on the 'cc_sur' tasks, and how the 'cc_sur' tasks are themselves dependent on previous 'gather' tasks.

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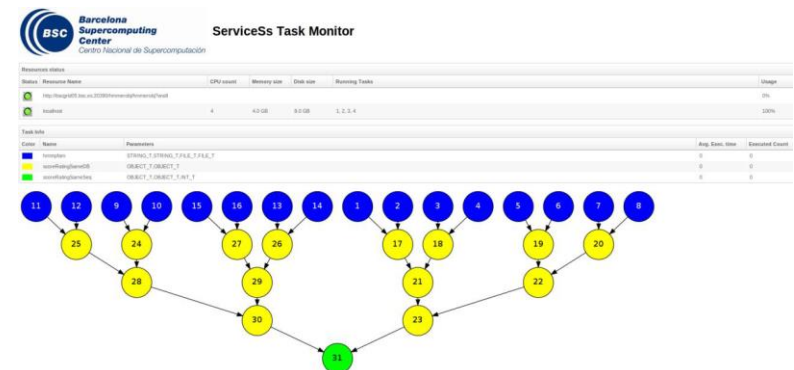
Transparent resilience at OpenMP

- NanoFT: Task based fault tolerance



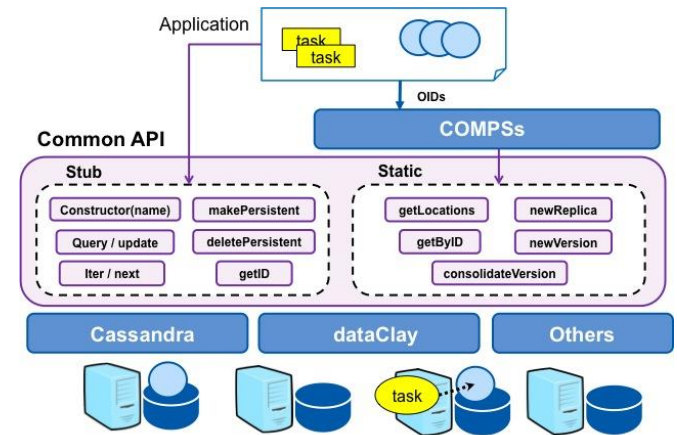
Workflow development environment

- COMPSs workflow in Java
- IDE for development and deployment



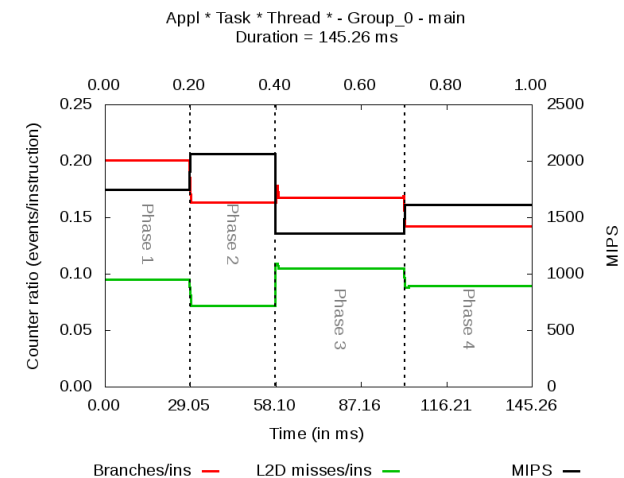
Integration of workflow and Data Infrastructures

- Persistent objects in Parallel Python
 - Cassandra and data clay



Instantaneous performance metrics

- With no noticeable overhead
- Example uses
 - Improving production codes
 - Analysing impact of multicore sharing

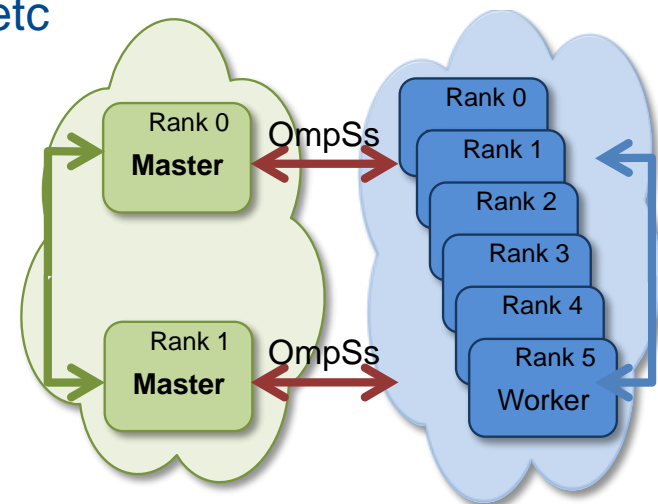


« Multilevel architectural simulation

- Integration Tasksim and Dimemas
- Trace-driven simulation of communications between nodes
- Execution driven simulation of node level runtime
- Trace driven simulation of core

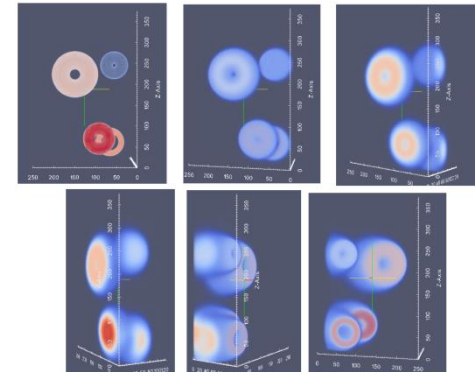
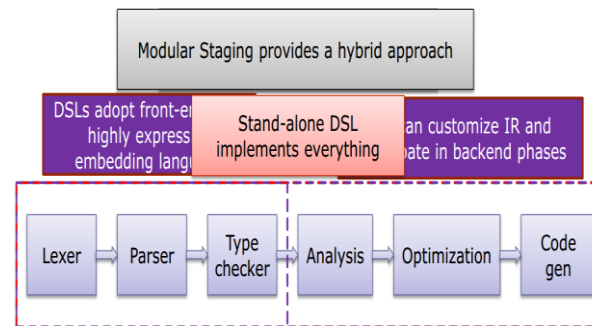
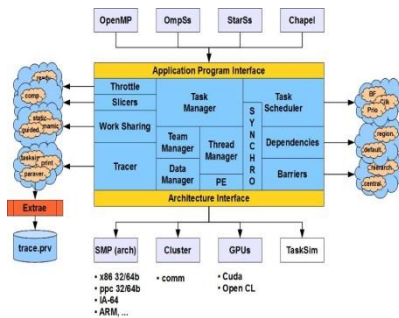
« Homogenizing heterogeneity

- Intra-node heterogeneity
 - OpenCL, CUDA, OpenMP 4.0, FPGAs, etc
- Cluster heterogeneity
 - OmpSs @ Cluster
 - OmpSs MPI offload



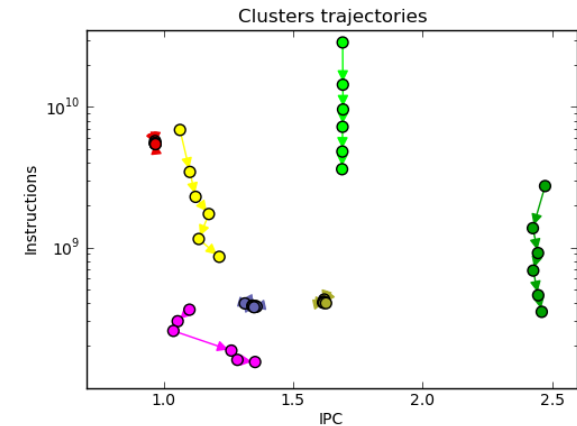
Generic DSL infrastructure

- Common framework to speedup development of HPC DSLs
 - OmpSs + Lightweight Modular Staging (LMS) + OpenCL
- Uses case: Saiph: A DSL for solving Partial Differential Equations



Tracking the performance of computation regions

- Changing core count ...
- ... problem size ...
- ... processes per node



Performance models

- Characterizing fundamental efficiency factors
- Scalability prediction for production codes

